UTILITY APPLICATION

Attorney Docket No.: NETL.001DV3

First Named Inventor: Jayesh R. Bhakta

Title: ARRANGEMENT OF INTEGRATED CIRCUITS IN A

MEMORY MODULE

Express Mail Label No.: EV 309090735 US

Direct all correspondence to Customer No.: 20995

Date: January 27, 2004

Page 1 of 2

Mail Stop Patent Application

United States Patent and Trademark Office PO Box 1450 Alexandria, VA 22313-1450

The following enclosures are transmitted herewith to be filed in the patent application of:

Inventors:

1. Jayesh R. Bhakta

2. Robert S. Pauley

APPLICATION ELEMENTS:

- (X) Specification in sixteen (16) pages.
- (X) Drawings in nine (9) sheets.
- (X) Declaration by Inventors in 1 page.

Copy from parent application

Reference to prior domestic applications is made in the:

Incorporation by Reference. The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied, is considered a part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

Examiner in prior application: Ly D. Pham Group Art Unit of prior application: 2818

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(X) Reference to prior domestic applications is made in the specification.

CONTINUITY INFORMATION:

Application	Relationship	Parent App. No.	Filing Date	Status	
This Application	Divisional of	10/094,512	03/07/02	Pending	
This Application	related to	10/674,082	09/29/03	Pending	
This Application	related to	10/674,240	09/29/03	Pending	
This Application	related to	Unknown Atty Docket No. NETL.001DV4	01/27/04	Pending	

(X) Incorporation by Reference. The entire disclosure of the prior applications is considered a part of the disclosure of the accompanying application and is hereby incorporated by reference therein.



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OTHER APPLICATION PARTS:

(X) Return prepaid postcard.

FILING FEES:

FEE CALCULATION									
FEE TYPE						FEE CODE	CALCULATION	TOTAL	
Basic Utility						1001 (\$770)		\$770	
Non-English Spec.						1053 (\$130)		0	
Recordation Fee						8021 (\$40)	0 x 40 =	0	
Excess Claims > 20	21	-	20	=	1	1202 (\$18)	1 x 18 =	18	
Independent > 3	3	-	3	=	0	1201 (\$86)	0 x 86 =	0	
Multiple Claim						1203 (\$290)		\$788	
							TOTAL FEE DUE	\$788	

(X) A check in the amount of \$788.00 to cover the Total Fee Due is enclosed.

The Commissioner is hereby authorized to charge any additional fees which may be required, now or in the future, or credit any overpayment to Account No. 11-1410.

Bruce S. Itchkawitz Registration No. 47,677 Attorney of Record

Customer No. 20,995 (949) 760-0404

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MAIL STOP PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

EV309090735US

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

Attorney Docket No. :

NETL.001DV3

Applicants

Jayesh R. Bhakta et al.

For

ARRANGEMENT OF INTEGRATED

CIRCUITS IN A MEMORY MODULE

Attorney

: Bruce S. Itchkawitz

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Mailing Label No.

EV 309090735 US

Date of Deposit

January 27, 2004

I hereby certify that the accompanying

Transmittal letter; specification in sixteen (16) pages; nine (9) sheets of drawings; Declaration by Inventor in 1 page; check for filing fee; return prepaid postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Nelson Merida

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